# CMPE-013/L

# Introduction to "C" Programming

Max Lichtenstein





Struct E (hrdnta Nole 5, Ze (Node)

# Roadmap

- Announcements
- Grades Review
- Battleboats Stuff
  - Rules
  - Field Module
  - Demo
- Timer Question

blow fast con too

- Randomness
- BREAK
- Engineering Tips from Max
- Software Design Principles





### **Announcements**

- Next week:
  - Still required!
- Magic H
- What to cover?
- Current "agenda":
  - · C dark arts
  - Tour of Board.c/h and other CMPE13 libraries
  - C vs C++, C#, other low-level languages
  - Hierarchical / Parallel State Machines
  - Events and Services: Queues and Priorities
- Taking requests!



#### **Announcements**

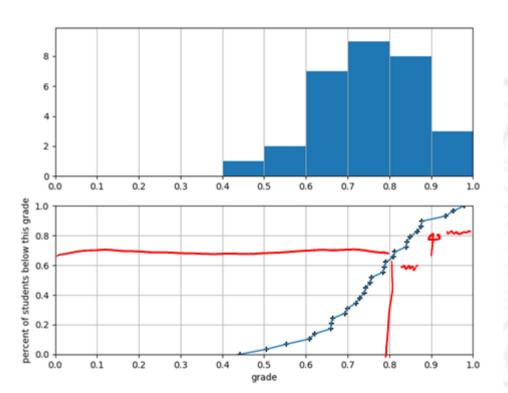
- Gitlab still "down"!
  - We have to live with it
  - Try disabling ssl verification in repo config (so much easier!)

```
git config --global http.sslVerify the
```





## **Grade stuff**

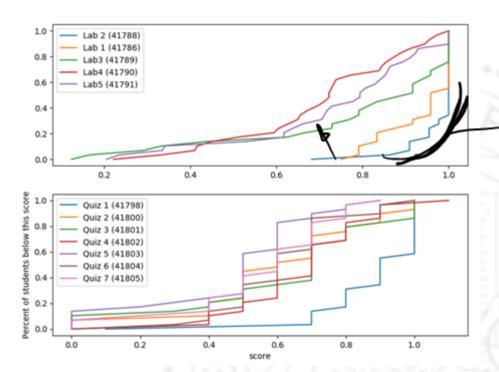




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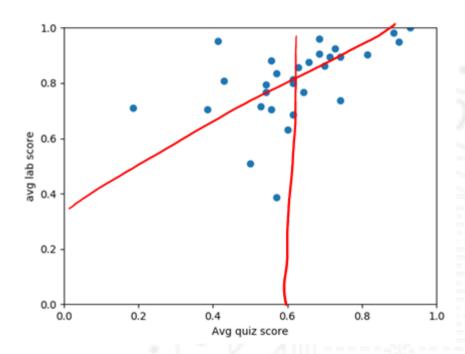
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## **Grade stuff**





## **Grade stuff**





# **Battleboats Stuff**



## **Battleboats Demo**





## **Battleboats Rules**

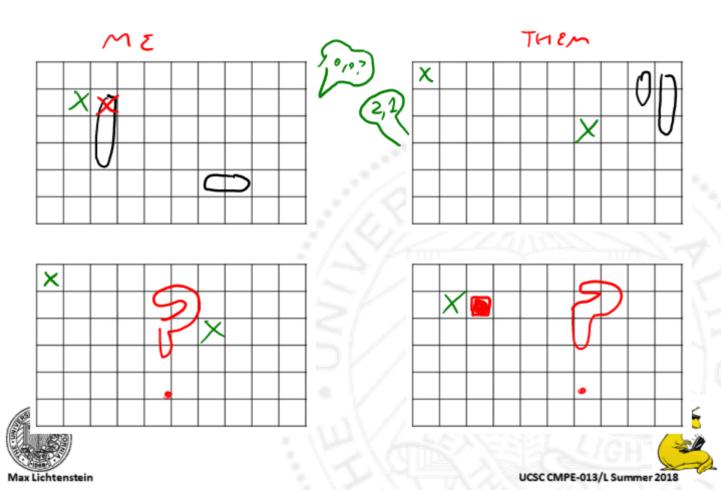
- A coin flip for first move
- Players place boats
- Each turn:
  - The attacking player makes a guess
  - The defending player describes the result
    - MISS, HIT, SINK a ship
  - BOTH players record the results
- The game is over when one player is out of ships.







## **Battleboats Rules**



## Field Module

```
911 [17][-1]
```

```
typedef enum {
    /// These denote field pos
    FIELD SQUARE EMPTY = 0,
    FIELD SQUARE SMALL BOAT,
    FIELD SQUARE MEDIUM BOAT,
    FIELD SQUARE LARGE BOAT,
    FIELD SQUARE HUGE BOAT,
    /// These denote field pos
    FIELD SQUARE UNKNOWN,
    FIELD SQUARE HIT,
    ///these statuses may be
    FIELD SQUARE MISS,
    /// This may be useful for
    FIELD SQUARE CURSOR,
    /// Occasionally, it may
    FIELD SQUARE INVALID,
  SquareStatus;
```

## Field struct

```
typedef struct {
    uint8_t grid[FIELD_ROWS][FIELD_COLS];
    uint8_t smallBoatLives;
    uint8_t mediumBoatLives;
    uint8_t largeBoatLives;
    uint8_t hugeBoatLives;
} Field;
```



## BattleBoats Tips

Design with visibility in mind

```
LOOK for tools alvoin there
```

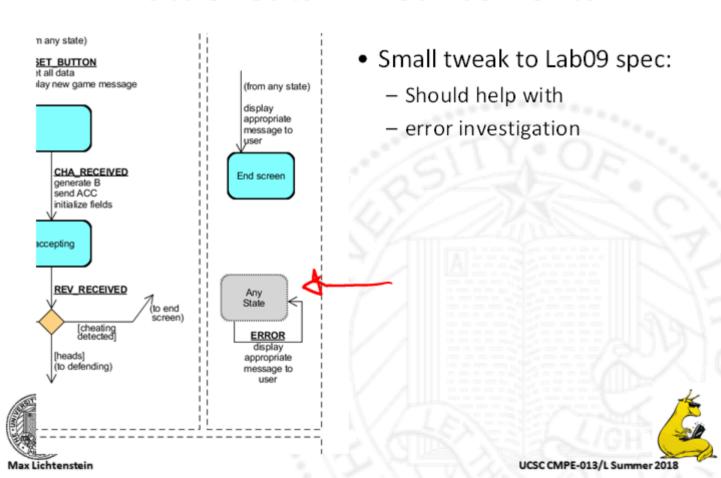
• Use params!

```
Error Eun-
1/#Jafine printf(...)
#Jesine
```

```
* Used to signal different types of errors a
  of a BattleBoat Error event. You are not
  but they can make error checking much more
typedef enum {
   BB SUCCESS = 0,
                                        //0
                                        //1
   BB ERROR BAD CHECKSUM,
                                        1/2
   BB ERROR PAYLOAD LEN EXCEEDED,
                                        //3
   BB ERROR CHECKSUM LEN EXCEEDED,
   BB ERROR INVALID MESSAGE TYPE,
                                        //4
   BB ERROR MESSAGE PARSE FAILURE,
 BB Error;
```



## **BattleBoats Announcements**



## **BattleBoats Announcements**

• You're *supposed* to work with your partner!



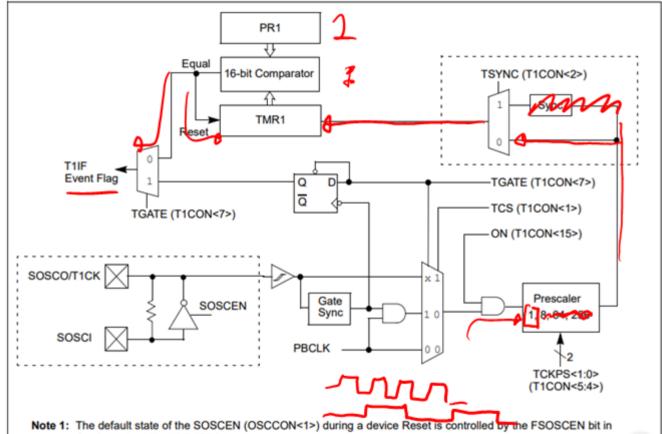


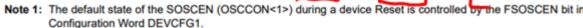
# How fast can an ISR go?





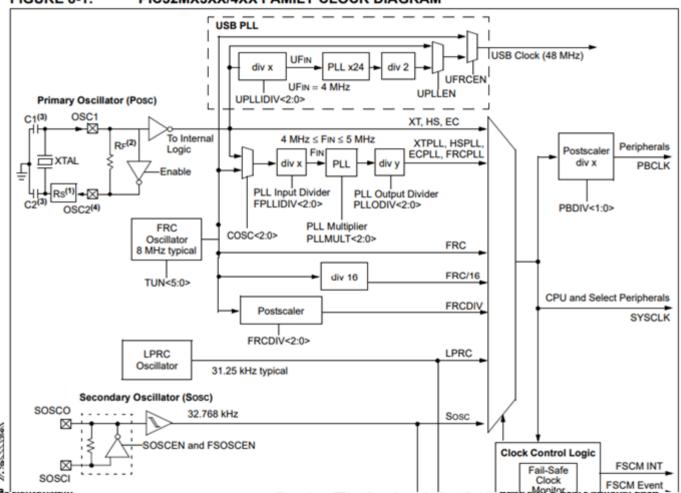
TIMER1 BLOCK DIAGRAM(1) **FIGURE 13-1:** 





#

#### FIGURE 8-1: PIC32MX3XX/4XX FAMILY CLOCK DIAGRAM



#### bit 18-16 PLLMULT<2:0>: Phase-Locked Loop (PLL) Multiplier bits

The POR default is set by the FPLLMUL<2:0> bits (DEVCFG2<6:4>). Do not change these bits if the PLL is enabled. Refer to the "Special Features" chapter in the specific device data sheet for details.

- 111 = Clock is multiplied by 24
- 110 = Clock is multiplied by 21
- 101 = Clock is multiplied by 20
- 100 = Clock is multiplied by 19
- 011 = Clock is multiplied by 18
- 010 = Clock is multiplied by 17
- 001 = Clock is multiplied by 16
- 000 = Clock is multiplied by 15

#### High-Performance 32-bit RISC CPU:

- MIPS32<sup>®</sup> M4K<sup>®</sup> 32-bit core with 5-stage pipeline
- 80 MHz maximum frequency
- 1.56 DMIPS/MHz (Dhrystone 2.1) performance at 0 wait state Flash access
- Single-cycle multiply and high-performance divide unit
- MIPS16e<sup>®</sup> mode for up to 40% smaller code size
- Two sets of 32 core register files (32-bit) to reduce interrupt latency
- Prefetch Cache module to speed execution from Flash





```
id __ISR(_TIMER_l_VECTOR, IPL4AUTO) TimerlHandler(void) {
    // Clear the interrupt flag.
    INTClearFlag(INT_T1);

    // If we've exceeded the timer trigger count, trigger a timer event.
    if (++timerData.value > SWITCH_STATES()) {
        timerData.event = true;
        timerData.value = 0;
    }
}
```

```
What happens when an interrupt occurs?
```

```
void ISR( TIMER 1 VECTOR, IPL4AUTO) TimerlHandler(void)
 0x9D0026F8: RDPGPR SP. SP
 0x9D0026FC: MFCO K1, EPC
 0x9D002700: MFCO KO, SRSCtl
 0x9D002704: ADDIU SP, SP, -120
 0x9D002708: SW K1, 116(SP)
 0x9D00270C: MFCO Kl, Status
 0x9D002710: SW KO, 108(SP)
 0x9D002714: SW K1, 112(SP)
 0x9D002718: INS K1, ZERO, 1, 15
 0x9D00271C: ORI K1, K1, 4096
 0x9D002720: MTCO Kl, Status
 0x9D002724: SW V1, 28(SP)
 0x9D002728: SW V0, 24(SP)
 0x9D00272C: LW V1, 108(SP)
 0x9D002730: ANDI V1, V1, 15
 0x9D002734: BNE V1, ZERO, 0x9D002780
 0x9D002738: NOP
 0x9D00273C: SW RA, 92(SP)
 0x9D002740: SW S8, 88(SP)
 0x9D002744: SW T9, 84(SP)
 0x9D002748: SW T8, 80(SP)
0x9D00274C: SW T7, 76(SP)
0x9D002750: SW T6, 72(SP)
 0x9D002754: SW T5, 68(SP)
```

N<sub>0x9D002758</sub>: SW T4, 64(SP)

```
0x9D002774: SW Al, 36(SP)
0x9D002778: SW AO, 32(SP)
0x9D00277C: SW AT, 20(SP)
0x9D002780: NOP
0x9D002784: MFLO VO
0x9D002788: SW VO, 100(SP)
0x9D00278C: MFHI VI
0x9D00279C: MFHI VI
0x9D00279C: SW VI, 96(SP)
0x9D002794: ADDU S8, SP, ZERO
! // Clear the interrupt flag.
! INTClearFlag(INT_T1);
0x9D002798: ADDIU AO, ZERO, 8
0x9D00279C: JAL INTClearFlag
0x9D0027AO: NOP
```

\$ 9it commit

\$ 9it tog Labor \_ submission\_U

\$ 9it push --tag

# Randomness



060120010 rand (); wint 16\_ \* = [0 .... (55357 Van)() >> 12 [ \* X ; rand()/,10 10 8 X= rm1 () 2712; 3while (x>10);

vint32 = Rand();  $\times &= rmd() << 16;$   $\times 100 000;$ 

## Randomness

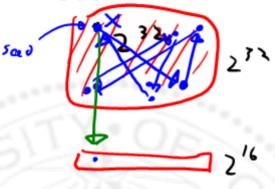
• How to do randomness on a deterministic machine?

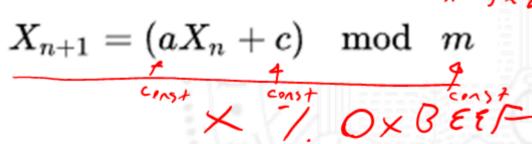


## **Pseudo-randomness**

- Start with a big, static variable
- Seed it
- Hash it









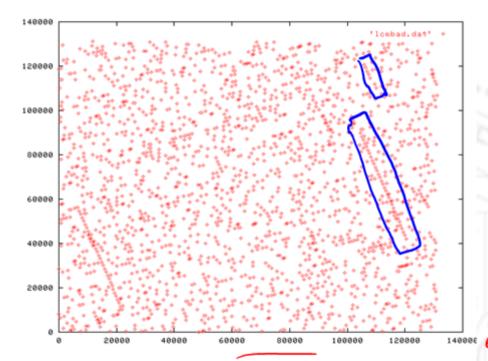
## Pseudo-randomness Issues

- It's a cycle!
  - Has a maximum period
  - If eavesdroppers can watch long enough, they can figure out where you are in your cycle
  - it can't shuffle a list very landom by
- Correlations still appear (usually)
  - DieHard battery





## Pseudo-randomness





## Real\* randomness

- Use (nearly) non-deterministic phenomena
  - Noise
  - Human interaction
  - Timer drift





## Randomness from noise



external sinsing















## Randomness from humans



Ken str. Ken



## Randomness from clock drift

2 clocks TUMBUTO TO THE TOTAL THE TOTAL TO THE TOTAL ISR ( lig timer)
read little timer



# **Combining randomness**

- XORing two random bitstrings
  - ALWAYS increases your entropy
  - Can combine many of these

A	8	OR	ANO	YOR
0	0	0	0	0
9	ſ	1 3 /	50/	
- 1	0	1 🖁 🖟	0	
(	1	1.5	* V	0



Main () {

BOARD\_ZMIT;

Srand (SWITCH\_STATES())





- Naming is important
  - That's why it's hard

top\_level\_event;

Functions should do one thing well

Message Parse Make Message struct

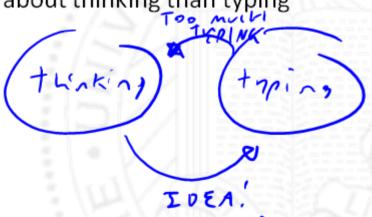




• Take breaks!

- Sleep - eat - Shiwor - go outside

Coding is more about thinking than typing





 Work on teams where you're the best, work on teams where you're the worst

Learn new languages

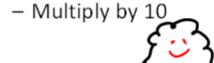


• Design systems with debugging in mind

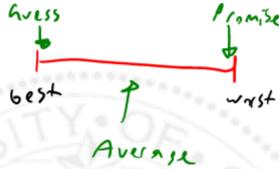
Learn new languages

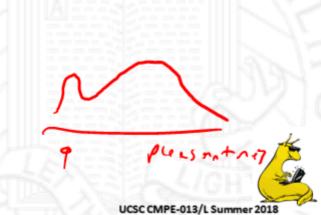


• Underpromise and overdeliver



Do it for love, not money







Help fix the culture

```
· Sexist , racist MAYBE

· Challenge your own binses

· think about users

· Engineers are full of turnselves
```



Design Build



Design process







- Use consistent styling
- Summary:
  - Utilize whitespace
  - Good variable/function names
  - Comments that describe non-obvious code behavior
    - "How?" and "why?" are good questions to answer in comments





- Modularity is important
- Why?
  - Supports code reuse
  - Simplifies changes
  - Allows for testing
- How?
  - Keep functions small
  - Minimize side effects
  - Information hiding/encapsulation





- Information hiding/encapsulation
- Summary:
  - Hide unimportant details from the user
  - Protects the user from breaking things
  - Separates backend from frontend





#### Mantras

- Keep it simple, stupid
  - KISS
- Summary:
  - Don't solve problems you don't need to
  - Don't introduce unnecessary complexity
  - Prioritize for readability and modularity
  - Don't be clever and/or cute
  - Applies to code architecture and specific code constructs





Mantras

- Don't repeat yourself
  - DRY
- Summary:
  - Write code only once
  - Simplifies refactoring/incremental development
  - Avoids copy/paste errors



Mantras

- You aren't gonna need it
  - YAGNI
- Summary:
  - Don't introduce features that are unnecessary
  - Don't write more code then you have to
  - Start small and build from there



- Principle of Least Astonishment
- Summary:
  - Be consistent with user's expectations
  - Build on user's intuition
  - Applies to users and developers
    - so both the code and library/program functionality
  - Lowers learning curve



Principle of Least Astonishment

- Functions/variables should have clear names
  - That should match their functionality!
  - Same for comments
- Functions should not do more than you would think
  - Minimize side effects
- Code should be grouped logically
- Functionality should follow precedence if any exists





- Garbage in, garbage out
- Summary:
  - "A system's output quality usually cannot be better than the input quality"
  - So bad input results in garbage output
    - Instead of an error condition
  - Can propagate through the system
  - Can be mitigated by checking the input data





- Fault tolerant design
- Summary:
  - Plan for operating failures
    - · Running out of memory
    - · Data being corrupted
  - Provide fallback modes
  - Important for complex software where minor errors can be common
  - Part of defensive programming



- Error tolerant design
- Summary:
  - Plan for user errors
    - "Fault tolerant design" applied to the human component
  - Primarily invalid user input
  - Important for complex software where minor errors can be common
  - Part of defensive programming



Writing fault/error tolerant code

- Check return values for errors!
  - Many functions have special return values when there are errors, these should usually be checked
  - File accesses
  - scanf()
  - malloc()
- Your code should have special error values
  - LinkedList library
- Program should also return error if failure





- Eating your own dogfood
- Summary:
  - When engineers use their own creations, they're generally better
  - More likely that bugs are fixed, features are added because they directly impact the developers
  - In use by all of industry
  - I do it



#### **Pitfalls**

- Premature Optimization
  - "root of all evil"
- Summary:
  - Optimizing code before performance is a critical factor
  - Optimizing reduces readability & modularity
  - Optimization not required for a lot of code
    - See Amdahl's Law
  - See KISS





Teamwork

- Working as a group is the most challenging engineering practice
- Requires:
  - Good communication
- That's it!



#### Teamwork

- Pair programming
- Summary:
  - Two developers work side by side: one driving, the other navigating
  - Just like driving:
    - Driver writes code
    - · Navigator plans ahead, thinks of edge cases, double-checks driver
  - Requires frequent role switching to be effective!



#### Teamwork

- Division of labor
- Summary:
  - Divide work into tasks that can be split between team members
  - Requires coordination to not step on each other's toes
  - Documentation is very important!
  - Can be useful to split testing and development between different people



